

Docket No.: M4065.0322/P322-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Eric T. Stubbs, et al.

Application No.: Not Yet Assigned

Group Art Unit: N/A

Filed: Concurrently Herewith

Examiner: Not Yet Assigned

For: INTEGRATED SEMICONDUCTOR
MEMORY CHIP WITH PRESENCE
DETECT DATA CAPABILITY

SUBMISSION OF FORMAL DRAWINGS

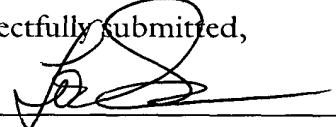
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (twelve sheets, twelve figures) of formal drawings for filing in the above-identified patent application.

Dated: 17 JUL 2003

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Michael Bergman

Registration No.: 42,318

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant